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Docket Number (Optional)
7103/30

In re Application of: Tafieh et al.

Application No. 08/853,323

Filed: May 8, 1997

For: Linear Polisher and Method for Semiconductor Wafer Planarization

The owner*, OnTrak Systems, Inc., of 100 percent interest in the instant application hereby disclaims, except as provided below, the terminal part of the statutory term of any patent granted on the instant application, which would extend beyond the expiration date of the full statutory term defined in 35 U.S.C. 154 to 156 and 173, as presently shortened by any terminal disclaimer, of prior Patent No. 5,692,947. The owner hereby agrees that any patent so granted on the instant application shall be enforceable only for and during such period that it and the prior patent are commonly owned. This agreement runs with any patent granted on the instant application and is binding upon the grantee, its successors or assigns.

In making the above disclaimer, the owner does not disclaim the terminal part of any patent granted on the instant application that would extend to the expiration date of the full statutory term as defined in 35 U.S.C. 154 to 156 and 173 of the prior patent, as presently shortened by any terminal disclaimer, in the event that it later expires for failure to pay a maintenance fee, is held unenforceable, is found invalid by a court of competent jurisdiction, is statutorily disclaimed in whole or terminally disclaimed under 37 CFR 1.321, has all claims canceled by a reexamination certificate, is reissued, or is in any manner terminated prior to the expiration of its full statutory term as presently shortened by any terminal disclaimer.

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2. The undersigned is an attorney of record.

7-7-98

Date

Signature

William A. Webb

Typed or Printed Name

- Terminal disclaimer fee under 37 CFR 1.20(d) is included.

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Date: July 8, 1998

To: Examiner George Nguyen
Art Unit: 3723
Fax No: 1 703 308 0163

From: William A. Webb
Tel. No: 1-312-321-4218

Client No: 7103

Serial No.: 08/853,323

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In re Application of:	Talieh et al.
Application No.	98/853,323
Filed:	May 8, 1997
For:	Linear Polisher and Method for Semiconductor Wafer Planarization

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